Intel Architecture Press Briefing

Stephen L. Smith

Vice President Director, Digital Enterprise Group Operations

> Ronak Singhal Principal Engineer Digital Enterprise Group

> > 17 March, 2008



Today's News Intel Technology: Delivering on the Promise

Mission Critical

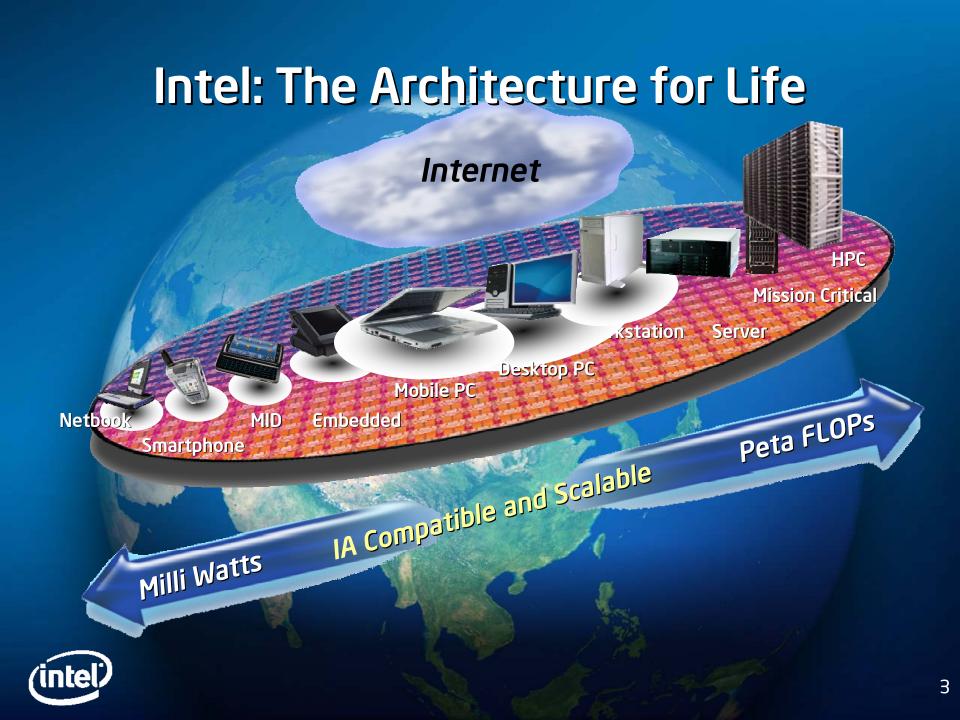
Tick-Tock

Expandable Server

Nehalem

Larrabee



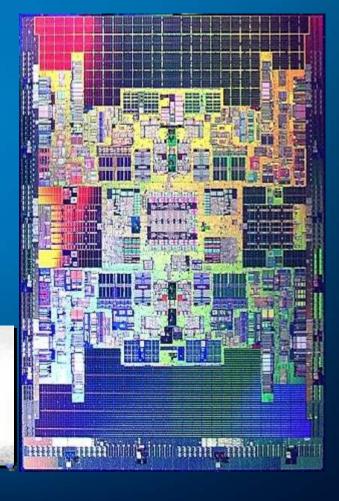


Tukwila: Delivering Performance to World's Most Powerful Computers

- Quad-core with 30 MB cache
- 2 billion transistors
- Multi-Threading Technology
- Intel QuickPath interconnect
- Dual Integrated Memory Controllers
- Estimate >2x* performance
- Mainframe-class RAS

"With Intel's upcoming quad-core Tukwila processor, Windows Server solutions running on Itanium-based systems will provide an even more scalable, reliable, agile and dynamic datacenter foundation for our customers."

-Bill Laing, GM Windows Server & Solutions Division, Microsoft





Product Cadence for Sustained Leadership

2007-08



TICK



Delivering Products on Schedule and Moore's Law



Expandable and Scalable: Quad-Core Intel[®] Xeon[®] processor 7300

- Caneland platform built for virtualization and consolidation
- Energy Efficient performance: Leading in benchmarks
- Scalable
- Enterprise proven reliability and investment protection
- Great customer acceptance

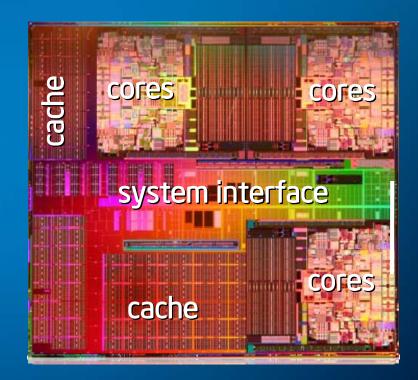


Industry's Virtualization Platform of Choice



Expandable and Scalable: Gets Better with Dunnington

- 6 core Processor
- 1.9 billion transistors
- 45nm Hi-K technology
- 16 MB L3 cache
- Latest Intel virtualization capabilities
- Socket compatible with Caneland platform
- Available 2H'08





Energy Efficiency: Top SPECpower* Results



Rank	Sponsor	SPECPower_ ssj2008 result	Platform	Processors
1	HP	778	DL180 G5	2x Intel® Xeon® E5450
2	Dell	719	PE 2950 III	2x Intel [®] Xeon [®] E5440
3	Dell	712	PE 1950 III	2x Intel [®] Xeon [®] E5440
4	HP	698	DL160 G5	2x Intel [®] Xeon [®] E5450
5	FSC	690	RX300 S4	2x Intel [®] Xeon [®] E5440
6	Dell	682	PE 2950	2x Intel [®] Xeon [®] E5440
7	FSC	667	TX150 S6	1x Intel [®] Xeon [®] X3220
8	HP	662	DL360 G5	2x Intel [®] Xeon [®] E5450
9	HP	546	DL580 G5	4x Intel [®] Xeon [®] L7345
10	Intel	468	SM 6025B	2x Intel [®] Xeon [®] L5335

First industry standard Energy Efficiency benchmark



Public SPECpower results from http://www.spec.org/power_ssj2008/results/power_ssj2008.html as of Feb 28, 2008 SPECPower_ssj2008 results measured as ssj_ops/watt

*SPEC, the SPEC logo and SPECpower are registered trademarks of the Standard Performance Evaluation Corporation

Product Cadence for Sustained Leadership

2007-08

Penryn Processors 45nm

TICK



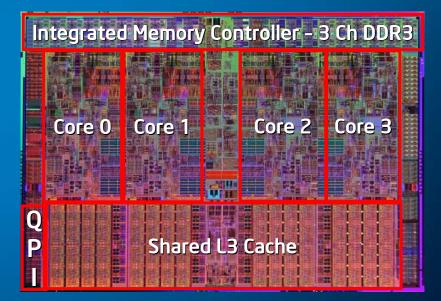
ТОСК



Driving Products to Deliver on Moore's Law

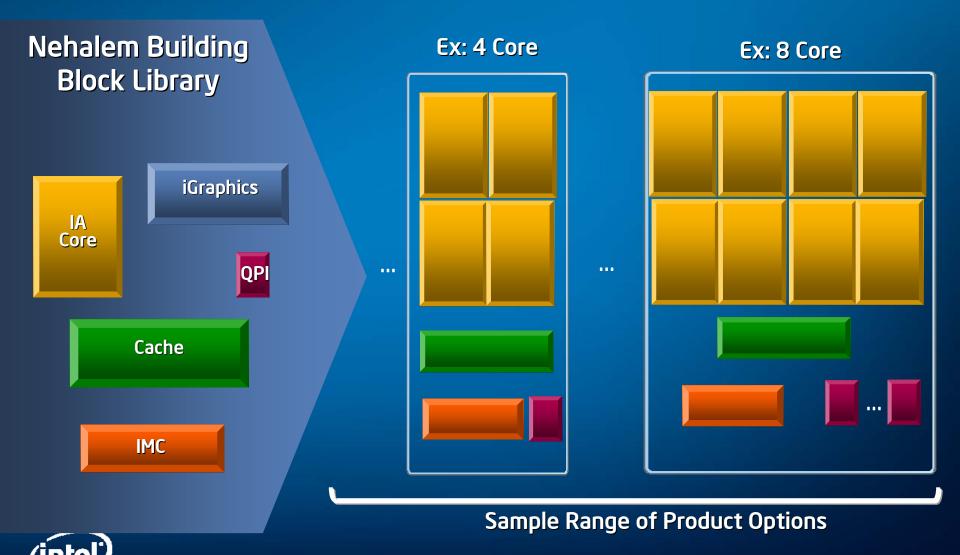
Nehalem Micro-architecture: Dynamically Scalable and Innovative New Design

Scalable from 2 to 8 cores Micro-architecture enhancements (4 – wide) 2-way simultaneous multi-threading Integrated memory controller OuickPath interconnect Shared and Inclusive Level-3 cache Dynamic power management SSE 4.2 Production: Q4'08





Nehalem Design Scalable Via Modularity

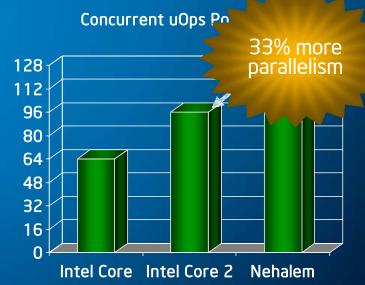


11 Block combinations are for illustration only and do not represent actual product plans. Block sizes are not indicative of die size contributions.

Nehalem: Core uArch Enhancements

Foundation: Intel[®] Core[™] Microarchitecture Significant Performance and Efficiency Enhancements

- Increased parallelism
 - > 33% more micro-ops in flight possible
- Enhanced algorithms
 - > Faster "unaligned" cache accesses
 - > Faster synchronization primitives
- Further branch prediction enhancements
 - > New 2nd level branch predictor
 - > Renamed Return Stack Buffer



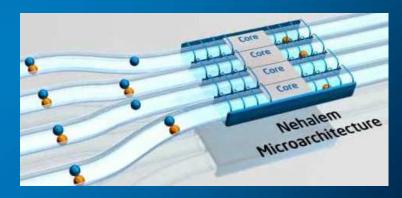
Builds upon Industry Leading 4 Instruction issue Intel[®] Core micro-architecture



Simultaneous Multi-Threading (SMT)

- Each core able to execute two software threads simultaneously
- Extremely power efficient
- Enhanced with larger caches and more memory bandwidth

• Benefits



- > Highly threaded workloads (eg, multi-media apps, databases, search engines)
- > Multi-Tasking scenarios

Simultaneous Multi-threading Enhances Performance and Energy Efficiency



Enhanced Cache Subsystem

New 3-level Cache Hierarchy

- > L1 cache same as Intel Core[™] uArch
 - 32 KB Instruction/32 KB Data
- > New 256 KB/core, low latency L2 cache
- > New Large 8MB fully-shared L3 cache
 - Inclusive Cache Policy minimize snoop traffic
- New 2-level TLB hierarchy
 - > Adds 2nd level 512 entry Translation Look-aside Buffer

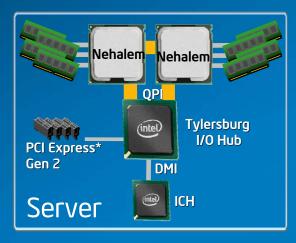


8 MB Last Level Cache

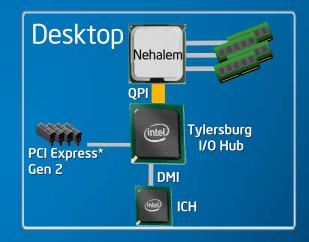
Superior multi-level shared cache extends Intel[®] Smart Cache technology



Nehalem/Tylersburg Platforms (High End Desktop and Server/Workstation)



- Intel[®] QuickPath Interconnect
 - New point to point interconnect
 - > 2 links per CPU socket
 - > Up to 25.6 Gb/sec total bandwidth/link



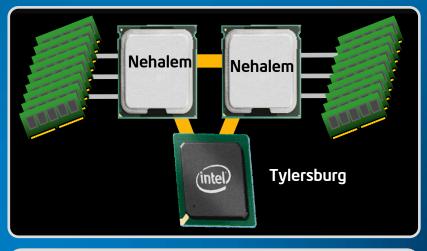
- Integrated DDR3 Memory Controller
 - > 3 channels per processor
 - Massive amounts of Bandwidth
 - Significant Memory Latency Reduction

Huge Latency Decrease and Bandwidth Increase over Prior Generation

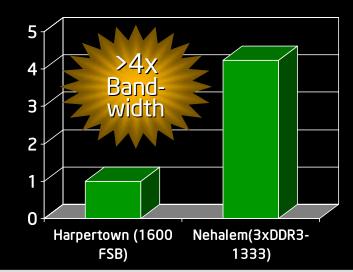


Nehalem High End Desktop/Server IMC

- 3 channels per socket
- Up to 3 DIMMs/channel
- DDR3-800, 1066, 1333
 > Future scalability
- Supports RDIMM and UDIMM
- Very low latency
- Very high bandwidth
- Built-In RAS Features



2 Socket Memory Bandwidth*





Leadership Memory Bandwidth

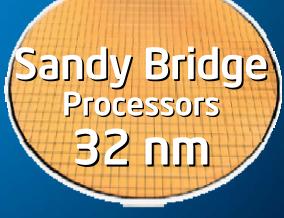
*Source: Intel internal measurement

Product Cadence for Sustained Leadership

2009-10

Westmere Processors 32 nm

TICK



ТОСК



Continuing the Pace of Innovation

Intel[®] Advanced Vector Extension (AVX) 256-bit vector extension to SSE for FP intensive applications

KEY FEATURES

BENEFITS

Wider Vectors Increased from 128 bit to 256 bit

Up to 2x peak FLOPs output

Enhanced Data

Rearrangement

Use the new 256 bit primitives to broadcast, mask loads and do data permutes

Organize, access and pull only necessary data more quickly and efficiently

Three Operand, Non Destructive Syntax Designed for efficiency and future extensibility Fewer register copies, better register use, more opportunities for parallel loads and compute operations, smaller code size



Visual Computing: Graphics Re-defined

<u>Mainstream Graphics</u>

• Triangle / Rasterization

• Rigid pipeline architecture

- Tools constrained by architecture
- Inefficient for non-graphics computing

Visual Computing

- New life-like Rendering e.g. Global illumination
- Programmable, ubiquitous architecture
- High definition audio and video processing
- Combines with model based computing (e.g. Physics)



Visual Computing

Acquiring, Analyzing, Modeling and Synthesizing Visual Workloads



Computational Modeling

High Definition Audio, Video





Interactive





Visual Computing: What Does it Take?

Intel Leadership



Platforms: Client, Workstation, Server
CPU, Graphics, Media Architecture
Process and Technology Leadership
Software, Tools & Developer Support

Photoreansuc 3D Rendering

> Interactive User Interface



High Definition Audio, Video

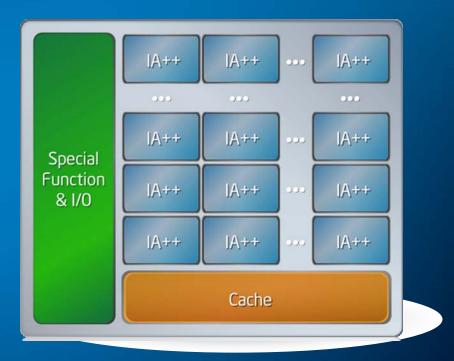
Computational

Modeling



Larrabee: Visual Computing Architecture

- Many IA cores
 Scalable to TeraFLOPS
- New cache architecture
- New vector instruction set
 - > Vector memory operations
 - Conditionals
 - Integer and FP arithmetic
- New vector processing unit / wide SIMD





Intel Software: Unleashes Developer Freedom

Industry Leading Intel[®] Software Tools Addresses development and performance tuning needs







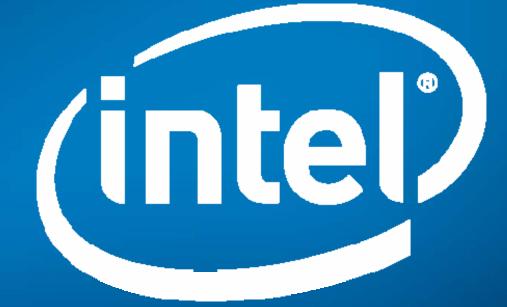
Visual Computing Tools & Resources Extending Intel® Software for Larrabee Architecture Supports industry standard APIs (DirectX* & OpenGL*)



*Other names and brands may be claimed as the property of others









Risk Factors

This presentation contains forward-looking statements. All statements made that are not historical facts are subject to a number of risks and uncertainties, and actual results may differ materially. Please refer to our most recent Earnings Release and our most recent Form 10-Q or 10-K filing available on our website for more information on the risk factors that could cause actual results to differ.



Risk Factors

This presentation contains forward-looking statements that involve a number of risks and uncertainties. These statements do not reflect the potential impact of any mergers, acquisitions, divestitures, investments or other similar transactions that may be completed in the future, with the exception of the Numonyx transaction. Our forward-looking statements for 2008 reflect the expectation that the Numonyx transaction will close during the first quarter. The information presented is accurate only as of today's date and will not be updated. In addition to any factors discussed in the presentation, the important factors that could cause actual results to differ materially include the following: Factors that could cause demand to be different from Intel's expectations include changes in business and economic conditions, including conditions in the credit market that could affect consumer confidence; customer acceptance of Intel's and competitors' products; changes in customer order patterns, including order cancellations; and changes in the level of inventory at customers. Intel's results could be affected by the timing of closing of acquisitions and divestitures. Intel operates in intensely competitive industries that are characterized by a high percentage of costs that are fixed or difficult to reduce in the short term and product demand that is highly variable and difficult to forecast. Additionally, Intel is in the process of transitioning to its next generation of products on 45 nm process technology, and there could be execution issues associated with these changes, including product defects and errata along with lower than anticipated manufacturing yields. Revenue and the gross margin percentage are affected by the timing of new Intel product introductions and the demand for and market acceptance of Intel's products; actions taken by Intel's competitors, including product offerings and introductions, marketing programs and pricing pressures and Intel's response to such actions; Intel's ability to respond quickly to technological developments and to incorporate new features into its products; and the availability of sufficient components from suppliers to meet demand. The gross margin percentage could vary significantly from expectations based on changes in revenue levels; product mix and pricing; capacity utilization; variations in inventory valuation, including variations related to the timing of qualifying products for sale; excess or obsolete inventory; manufacturing yields; changes in unit costs; impairments of long-lived assets, including manufacturing, assembly/test and intangible assets; and the timing and execution of the manufacturing ramp and associated costs, including start-up costs. Expenses, particularly certain marketing and compensation expenses, vary depending on the level of demand for Intel's products, the level of revenue and profits, and impairments of long-lived assets. Intel is in the midst of a structure and efficiency program that is resulting in several actions that could have an impact on expected expense levels and gross margin. We expect to complete the divestiture of our NOR flash memory assets to Numonyx. A delay or failure of the transaction to close, or a change in the financial performance of the contributed businesses could have a negative impact on our financial statements. Intel's equity proportion of the new company's results will be reflected on its financial statements below operating income and with a one quarter lag. Intel's results could be affected by the amount, type, and valuation of share-based awards granted as well as the amount of awards cancelled due to employee turnover and the timing of award exercises by employees. Intel's results could be impacted by adverse economic, social, political and physical/infrastructure conditions in the countries in which Intel, its customers or its suppliers operate, including military conflict and other security risks, natural disasters, infrastructure disruptions, health concerns and fluctuations in currency exchange rates. Intel's results could be affected by adverse effects associated with product defects and errata (deviations from published specifications), and by litigation or regulatory matters involving intellectual property, stockholder, consumer, antitrust and other issues, such as the litigation and regulatory matters described in Intel's SEC reports. A detailed discussion of these and other factors that could affect Intel's results is included in Intel's SEC filings, including the report on Form 10-K for the fiscal year ended December 29, 2007.



[Instruction: If copies of the presentation materials are going to be distributed (printout, CD, etc.) or made available for download, this risk factor slide needs to be included in the printout/file. This slide does not need to be read, or shown on the screen at the end of the presentation.]

Legal Disclaimer

- INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL® PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER, AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL® PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT. INTEL PRODUCTS ARE NOT INTENDED FOR USE IN MEDICAL, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS.
- Intel may make changes to specifications and product descriptions at any time, without notice.
- All products, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice.
- Intel, processors, chipsets, and desktop boards may contain design defects or errors known as errata, which may cause the product to deviate from published specifications. Current characterized errata are available on request.
- Performance tests and ratings are measured using specific computer systems and/or components and
 reflect the approximate performance of Intel products as measured by those tests. Any difference in
 system hardware or software design or configuration may affect actual performance.
- Intel, Intel Inside, and the Intel logo are trademarks of Intel Corporation in the United States and other countries.
- *Other names and brands may be claimed as the property of others.
- Copyright ° 2008 Intel Corporation.



Legal Notices

Performance tests and ratings are measured using specific computer systems and/or components and reflect the approximate performance of Intel products as measured by those tests. Any difference in system hardware or software design or configuration may affect actual performance. Buyers should consult other sources of information to evaluate the performance of systems or components they are considering purchasing. For more information on performance tests and on the performance of Intel products, visit http://www.intel.com/performance/resources/limits.htm or call (U.S.) 1-800-628-8686 or 1-916-356-3104. All dates and products specified are for planning purposes only and are subject to change without notice

Relative performance is calculated by assigning a baseline value of 1.0 to one benchmark result, and then dividing the actual benchmark result for the baseline platform into each of the specific benchmark results of each of the other platforms, and assigning them a relative performance number that correlates with the performance improvements reported.

SPEC, SPECint2000, SPECfp2000, SPECint2006, SPECfp2006 are trademarks of the Standard Performance Evaluation Corporation. See http://www.spec.org for more information.

Intel® Virtualization Technology requires a computer system with an enabled Intel® processor, BIOS, virtual machine monitor (VMM) and, for some uses, certain platform software enabled for it. Functionality, performance or other benefits will vary depending on hardware and software configurations and may require a BIOS update. Software applications may not be compatible with all operating systems. Please check with your application vendor.

Intel processor numbers are not a measure of performance. Processor numbers differentiate features within each processor series, not across different processor sequences. See http://www.intel.com/products/processor_number for details. Intel products are not intended for use in medical, life saving, life sustaining, critical control or safety systems, or in nuclear facility applications. All dates and products specified are for planning purposes only and are subject to change without notice * Other names and brands may be claimed as the property of others.

Copyright © 2007 Intel Corporation. All rights reserved. Intel, the Intel logo, Xeon and Intel Core are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.



Performance Backup

 Source: Intel. Configuration: Intel[®] Core[™] 2 Quad Q9450 (12MB L2, 2.66 GHz, 1333MHz FSB), Intel[®] Core[™] 2 Quad Q8400 (6MB L2, 3.00 GHz, 1333MHz FSB), E6750 (4MB L2, 2.66 GHz, 1333MHz FSB) and Q6600 (8MB L2, 2.40 GHz, 1066MHz FSB) on Intel Desktop DG33TL board with Intel G33 Integrated Graphics, Intel Chipset INF 8.4.0.1016,, 2x1GB Dual Channel Corsair* DDR2-800 5-5-5-18, Seagate* 320GB Barracuda* NCQ Serial ATA 7200 RPM, Windows* Vista* Ultimate 32bit. Performance tests and ratings are measured using specific computer systems and / or components and reflect the approximate performance of Intel products as measured by those tests. Any difference in system hardware or software design or configuration may affect actual performance. Buyers should consult other sources of information to evaluate the performance of systems or components they are considering purchasing. For more information on performance tests and on the performance of Intel products, visit http://www.intel.com/performance/

